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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patent Application

Inventor(s):

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Application No.:

09/975,030

Group Art Unit:

2123

Filed:

10/10/01

Examiner:

Dwin M. Craig

Title:

EMULATOR CHIP-BOARD ARCHITECTURE AND INTERFACE

Form 1449

U.S. Patent Documents

Examiner Initial	No.	Patent No.	Date	Patentee	Class	Sub- class	Filing Date
Ano	Α	6,564,179	05/13/03	Belhaj	703	26	07/26/99
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Foreign Patent or Published Foreign Patent Application

Examiner	T	Document	Publication	Country or Patent Office		Sub-	Trans	lation
Initial	No.	No.	Date	Patent Office	Class	class	Yes	No
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Other Documents

Examiner Initial	No.	Author, Title, Date, Place (e.g. Journal) of Publication
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Examiner	Mi	Date Considered 10-13-05

Examiner: Initial citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.